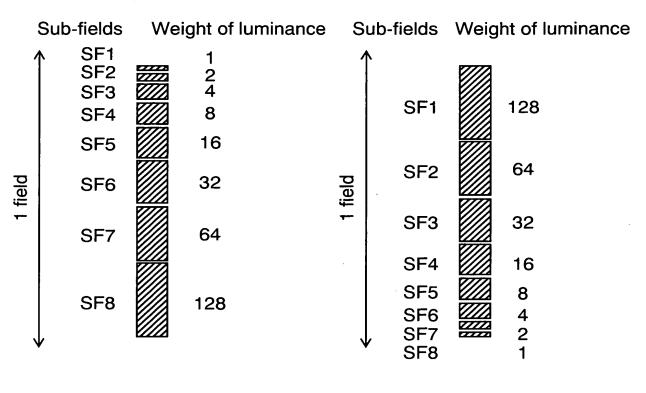
FIG. 1A

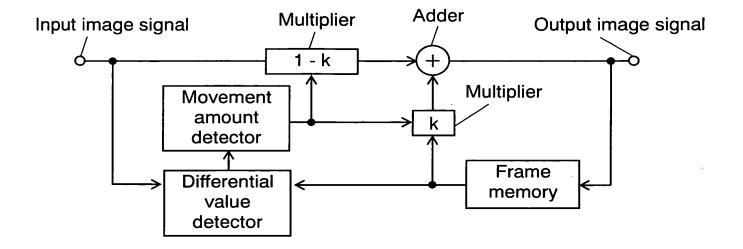
FIG. 1B



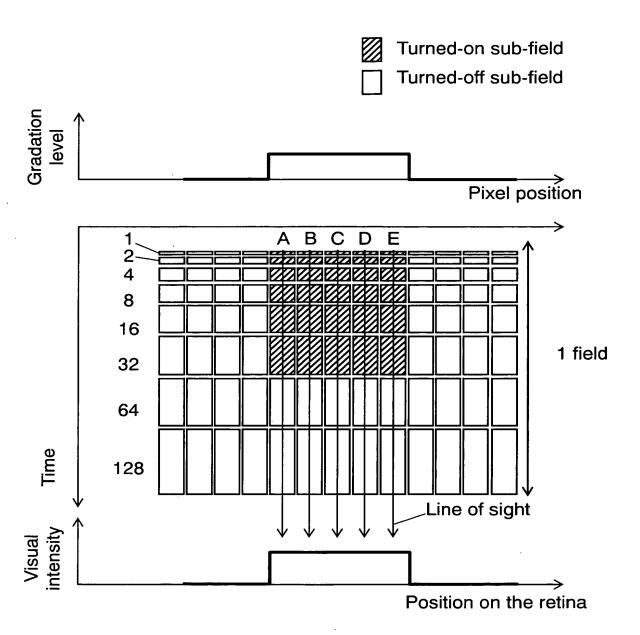
Ascending coding

Descending coding

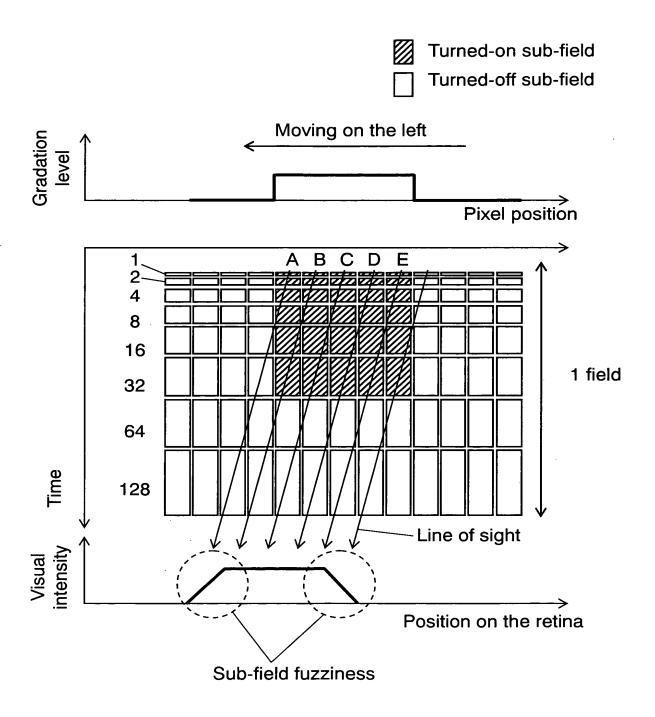
2/13 FIG. 2



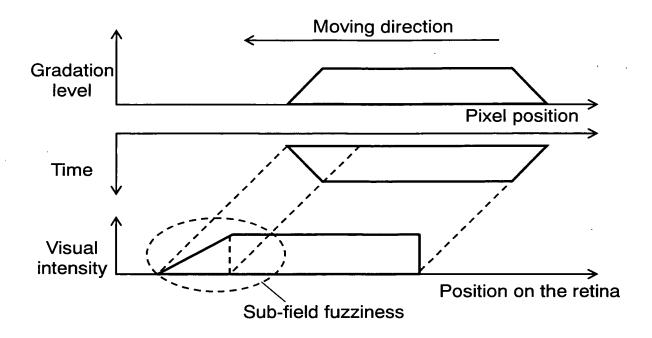
3/13 FIG. 3

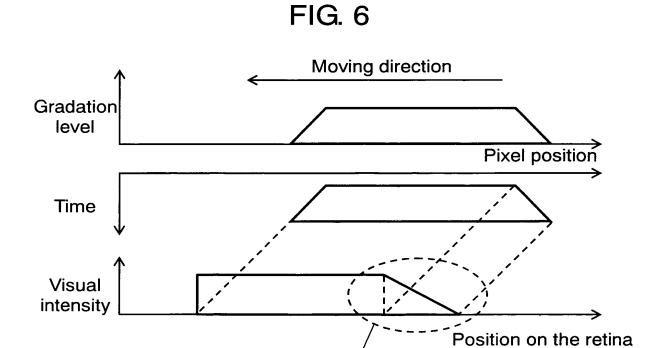


4/13 FIG. 4



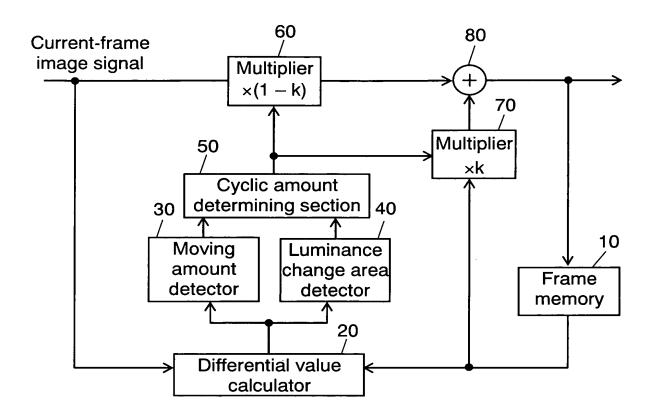
5/13 **FIG**. 5

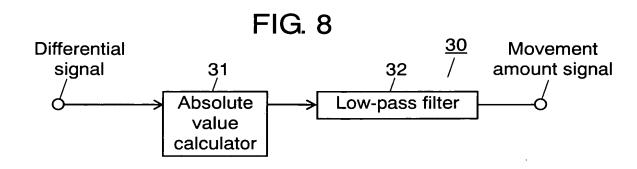


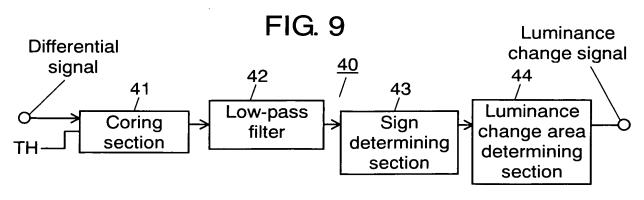


Sub-field fuzziness

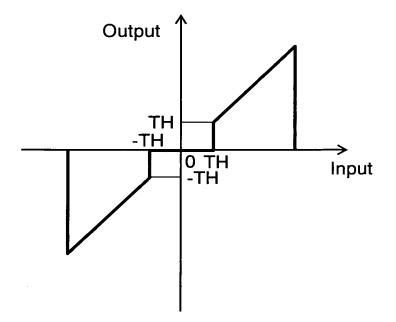
6/13 **FIG**. 7







7/13 FIG. 10



8/13 FIG. 11A

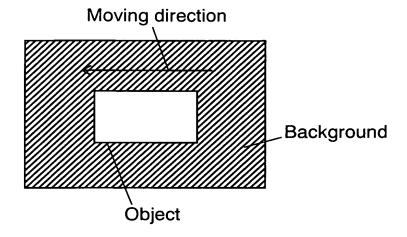
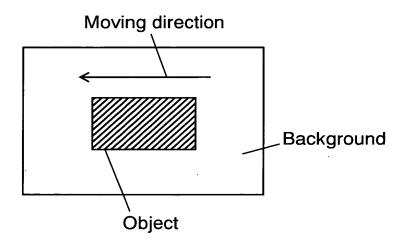
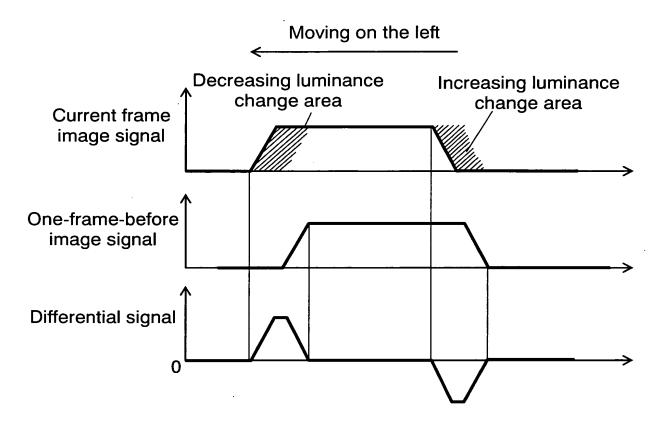


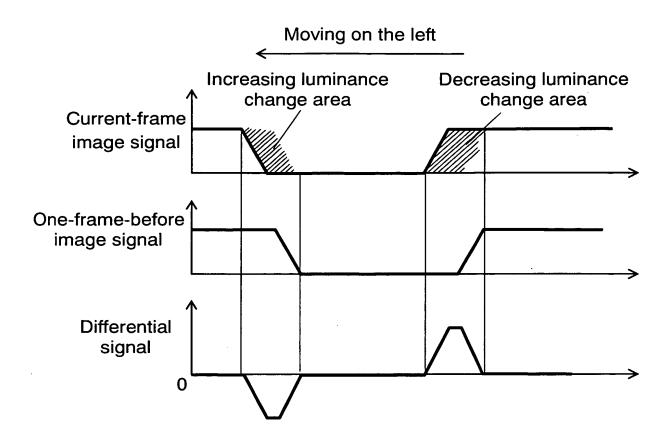
FIG. 11B



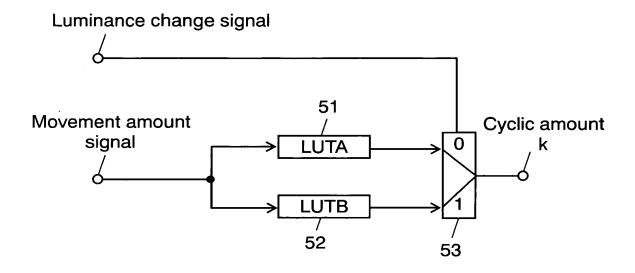
9/13 FIG. 12



10/13 FIG. 13



11/13 FIG. 14

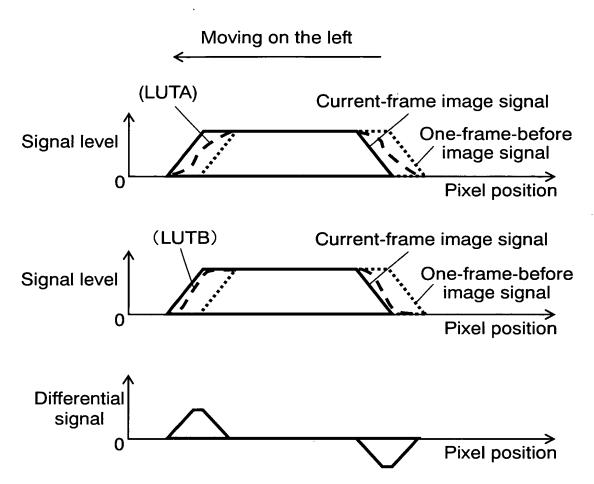


Cyclic amount k

Cyclic amount k

Movement amount signal

12/13 FIG. 16



13/13

Reference numerals in the drawings

| 10 | Trame memory |
|-------|---|
| 20 | differential value calculator |
| 30 | movement amount detector |
| 31 | absolute value calculator |
| 32, 4 | 42 low-pass filter |
| 40 | luminance change area detector |
| 41 | coring section |
| 43 | sign determining section |
| 44 | luminance change area determining section |
| 50 | cyclic amount determining section |
| 51 | LUTA |
| 52 | LUTB |
| 53 | selector |
| 60, 7 | 70 multiplier |
| 80 | adder |